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Sheet	1	of	1	GROUP	ART UNIT:	2813	EXAMINER	D.A. Wo	lverton	
Examiner's Initials#	Cite No.	U.S.	Patent Docur		U.S. PATENT DOCUMENTS Name of Patentee or Applicant of Cited Documen			t Date of Publication or Issue of Cited Document MM-DD-YYYY		
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		Office/ Country	Number	Kind Code			
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OTHER ART - NON PATENT LITERATURE DOCUMENTS

Examiner's	Cite	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item	Translation (Y/N)	
Initials #	No	(book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
		FRIEDMAN, Robin S. et al. "High-speed integrated nanowire circuits" NATURE, Vol. 434, April 28,		
		2005, page 1085		
		IBM News, 2005, "IBM creates highest performing nanotube transistors"		
		www.ibm.com/news/us/2002/05/20.html		
		Office Action from Serial No. 11/543,337 dated March 18, 2008		

EXAMINER:	DATE CONSIDERED:	
/Daren Wolverton/	01/14/2010	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. ______, filed ______, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

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